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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/692,665 10/24/2003		Nitin Muppalaneni	5693P027	9957
48102	7590 11/22/2006		EXAMINER	
	APPLIANCE/BLAK	DOAN,	DOAN, DUC T	
12400 WILS SEVENTH I	HIRE BLVD LOOR		ART UNIT	PAPER NUMBER
LOS ANGE	LES, CA 90025-1030	2188		
		DATE MAILED: 11/22/2006		

Please find below and/or attached an Office communication concerning this application or proceeding.

-			ication No.	Applicant(s)				
Office Action Commence		10/6	92,665	MUPPALANENI E	ET AL.			
Office Action Summary			niner	Art Unit				
			T. Doan	2188				
Period fo	The MAILING DATE of this commun r Reply	nication appears o	on the cover sheet with the	correspondence a	ddress			
WHIC - Exter after - If NC - Failu Any	ORTENED STATUTORY PERIOD FOR CHEVER IS LONGER, FROM THE Masions of time may be available under the provision SIX (6) MONTHS from the mailing date of this come period for reply is specified above, the maximum is reto reply within the set or extended period for reply reply received by the Office later than three months and patent term adjustment. See 37 CFR 1.704(b).	MAILING DATE C s of 37 CFR 1.136(a). In munication. tatutory period will apply y will, by statute, cause t	OF THIS COMMUNICATION on o event, however, may a reply be to and will expire SIX (6) MONTHS from the application to become ABANDONI	N. mely filed n the mailing date of this of ED (35 U.S.C. § 133).				
Status		•						
1)⊠	Responsive to communication(s) fil	ed on <u>11 Septem</u>	<u>ber 2006</u> .					
2a)□	This action is FINAL .	2b)⊠ This action	s action is non-final.					
3)	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is							
	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.							
Dispositi	ion of Claims							
4)🖂	4)⊠ Claim(s) <u>1-34</u> is/are pending in the application.							
	4a) Of the above claim(s) is/are withdrawn from consideration.							
5)	5) Claim(s) is/are allowed.							
-	Discription (S) 1-34 is/are rejected. Discription (S) 1-34 is/are objected to.							
•								
8)[_]	Claim(s) are subject to restri	ction and/or elec	tion requirement.					
Applicat	ion Papers							
9)	The specification is objected to by the	ne Examiner.						
10)	10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner.							
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).								
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).								
11)	The oath or declaration is objected	to by the Examine	er. Note the attached Offic	e Action or form P	TO-152.			
Priority (ınder 35 U.S.C. § 119							
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).								
a)	a) All b) Some * c) None of:							
	 Certified copies of the priority documents have been received. Certified copies of the priority documents have been received in Application No 							
	3. Copies of the certified copies of the priority documents have been received in this National Stage							
	application from the International Bureau (PCT Rule 17.2(a)).							
* See the attached detailed Office action for a list of the certified copies not received.								
Attachmen	t(s)		•					
	ce of References Cited (PTO-892)		4) Interview Summar	y (PTO-413)				
2) Notice	e of Draftsperson's Patent Drawing Review (Paper No(s)/Mail [Date				
	mation Disclosure Statement(s) (PTO/SB/08) er No(s)/Mail Date		5) Notice of Informal 6) Other:	ratent Application				

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DETAILED ACTION

Status of Claims

Claims 1-34 have been presented for examination in this application. In response to the last office action, claim 24 has been amended. As the result, claims 1-34 are pending in this application.

The applicant's remarks filed 9/11/06 have been considered with the results that follow, Examiner withdraws previous rejections and applying new rejections with new reference(s) found.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

A person shall be entitled to a patent unless -

- (a) the invention was known or used by other's in this country or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.
- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- (e) the invention was described in a patent granted on an application for patent by another fled in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

Claims 1,8,13-15,19,24-26,30,32-34 are rejected under 35 U.S.C. 102 (a) as being anticipated by Tehloh et al (US 2003/0014523).

As in claim 1, Tehloh discloses a method for mirroring data on a first storage server and a second storage server, the method comprising: queuing write commands at the first storage server between consistency points, the write commands being to write data corresponding to a file system of the first storage server to a local mass storage device coupled to the first storage server (Tehloh's Fig 1 discloses data replicating (i.e mirroring paragraph 44) in which data and commands are queued in the local replication facility; subsequently data is written to local storage (paragraph 45, Fig2: #24);

at a start of each consistency point, sending the write commands to the local mass storage device and to a remote mass storage device coupled to the second storage server; updating memory blocks of the local and remote mass storage devices based on the write commands (Tehloh's Fig 2: #34, #42 discloses writing data to the local and remote storage); and

Tehloh's further disclose at an end of each consistency point constructing a representation to reference each memory block of the local mass storage device that is in use to represent the file system (Tehloh's parargraph 45 discloses building the data structures representing memory blocks such as bit map); and sending at least a portion of the representation to the second storage server (Tehloh's paragraph 46 discloses sending this replicated data package information that identifies the storage locations of memory blocks including information such as volume path).

Claims 8,19 rejected based on the same rationale as of claim 1.

As in claim 13, Tehloh discloses receiving high-level write requests from a client served by the first storage server, the high-level request requires changes to the file system (Tehloh's Fig 1 discloses local storage server Fig 1: 20 receiving a write requests to data in a file stored at volume #100 in local storage device #24); generating block-level write commands to update memory blocks of a local mass storage device coupled to the first storage server, based on the high-level write request; queuing the block-level write commands (Tehloh's paragraph 48 discloses queuing write commands and subsequently issue write commands to local storage devices to write and updating data blocks; and sending the data write package to the remote storage (Tehloh's paragraph 46)).

As in claim 14, Tehloh's discloses the receiving and the updating is performed at each consistency point (Tehloh's paragraph 46 lines 13-24).

As in claim 15, Tehloh discloses receiving at the end of each consistency point, a representation of each memory block currently in use to represent an active state of the file system (Tehloh's paragraph 46 discloses the remote storage receiving the data package information including the information such as volume path that representing the active state of the file system).

Claim 24 rejected based on the same rationale as of claim 13.

Claims 25,33 rejected based on the same rationale as of claim 14.

Claim 26 rejected based on the same rationale as of claim 15.

Claim 30 rejected based on the same rationale as of claim 8.

Claim 32 rejected based on the same rationale as of claim 13.

As in claim 34, Tehloh discloses in the first storage server, constructing a representation to reference each memory block used to store a file system, the memory blocks being part of the first set of mass storage devices (Tehloh's paragraphs 45-46 discloses constructing data structures that represent the referencing to memory blocks in a file system (i.e bit map and volume path), the memory blocks are stored in the first mass storage device (Fig 1: #24),

Tehloh further discloses sending at least a portion of the representation to a second storage server, said portion comprising information to allow reconstruction of the entire representation by the second storage server so that the second storage server has a representation of memory blocks of a second set of mass storage devices comprising at least one mass storage device coupled locally to the second storage server used to store the file system (Tehloh's paragraph 46 discloses the local data replication facility sending information in the data package including volume path and the data to the remote data replication facility so that the data can be stored in the second set of mass storage device Fig 1: #26, #102).

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

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Claims 2-7,9-12,16-18,20-23,27-29,31,35 are rejected under 35 U.S.C. 103(e) as being unpatentable over Tehloh et al (US 2003/0014523) as applied to claims 1, 8,14,15,19,26,30,34 and further in view of Gutherie (2005/0010592).

Tehloh does not expressly disclose the details in claims 2-4. However, Guthrie disclose a snapshot system wherein the memory blocks of storage devices are represented by hierarchical files/nodes including directory nodes (i.e nodes/memory blocks with address pointers) and leaf data blocks in a file system (paragraph 16), these nodes includes the root nodes for the multiple snapshot copies, these nodes are organized as sub-tree in a hierarchical manner (Fig 7, Gutherie's paragraph's 16 lines 7-11). It would have been obvious to one of ordinary skill in the art at the time of invention to include the hierarchical structures to representing the memory blocks being used in copying as suggested by Gutherie in Tehloh's system such that only root nodes and nodes that are being modified are copied (Gutherie's paragraph 16 lines 26-30), thereby the snapshot procedure can be done with significant saving in space and time (Gutherie's paragraph 3 lines 18-23).

As in claim 5, Tehloh's paragraph 46 discloses sending this replicated data package information that identifies the storage locations of memory blocks including information such as volume path (i.e contains root node information). Notes that the detail of using root nodes in the snapshot procedure is further disclosed by Gutherie's Fig 7, paragraph 17).

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As in claims 6-7, Tehloh does not expressly disclose the claims' detail limitation, however, Gutherie discloses a scheme in which a memory block referenced within a representation is not overwritten (Gutherie's paragraph 3); allowing read-only access to the remote mass storage device while updating the memory blocks of the remote mass storage (Gutherie's paragraph 26 discloses by using the snapshot hierarchical nodes as disclosed in Fig 5, the data access from the host can be served by traversing the roots nodes and determines which data blocks being updating (i.e current snapshot), and which data blocks have been snapshot in previous snapshot points).

Claims 9-10 rejected based on the same rationale as of claim 3.

Claim 11 rejected based on the same rationale as of claim 4.

Claims 12,23 rejected based on the same rationale as of claim 5.

Claims 16,27 rejected based on the same rationale as of claim 6.

As in claim 17, Tehloh does not disclose the claim's detail. However, Gutherie's Fig 7 discloses the representation comprises a root node for a tree that includes nodes representing the memory block currently in use by the file system (Fig 7: #701 file system to be snapshot). It would have been obvious to one of ordinary skill in the art at the time of invention to include the hierarchical structures to representing the memory blocks being used in copying as suggested by Gutherie in Tehloh's system such that only root nodes and nodes that are being modified are copied (Gutherie's paragraph 16 lines 26-30), thereby the snapshot procedure can be done with significant saving in space and time (Gutherie's paragraph 3 lines 18-23).

Claims 18,29 rejected based on the same rationale as of claim 2.

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As in claim 20, Gutherie's Fig 7 discloses the representation comprises a sub-tree structure in which each node references a memory block in use by the system (Fig 7, paragraph 16, data blocks stored in files referenced by nodes).

As in claim 22, Gutherie's paragraph 16 discloses each sub-tree structure corresponding to a representation constructed at a particular consistency point is root in a root node (each snapshot representing at a particular consistency point a root node of the sub-tree of nodes involved in the snapshot, see paragraph 16).

Claims 28,31 rejected based on the same rationale as of claim 17.

As in claim 35, Tehloh does not disclose the claim's detail, However Gutherie discloses a method maintaining multiple snapshots data structures (Fig 7: #701, #702), such that synchronization can be readily processed for each snapshot by traversing the snapshots tree structures (Gutherie's paragraph 26). It would have been obvious to one of ordinary skill in the art at the time of invention to include the hierarchical structures to representing the memory blocks being used in copying as suggested by Gutherie in Tehloh's system such that only root nodes and nodes that are being modified are copied (Gutherie's paragraph 16 lines 26-30), thereby the snapshot procedure can be done with significant saving in space and time (Gutherie's paragraph 3 lines 18-23).

Response to Arguments

Applicant's arguments with respect to claims 1-34 have been considered but are most in view of the new ground(s) of rejection.

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Conclusion

When responding to the office action, Applicant is advised to provide the examiner with the line numbers and page numbers in the application and/or references cited to assist examiner to locate the appropriate paragraphs.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Duc T. Doan whose telephone number is 571-272-4171.

The examiner can normally be reached on M-F 8:00 AM 05:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Hyung S. Sough can be reached on 571-272-6799. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Duc Doan

Examiner

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Supervisory Patent Examiner

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